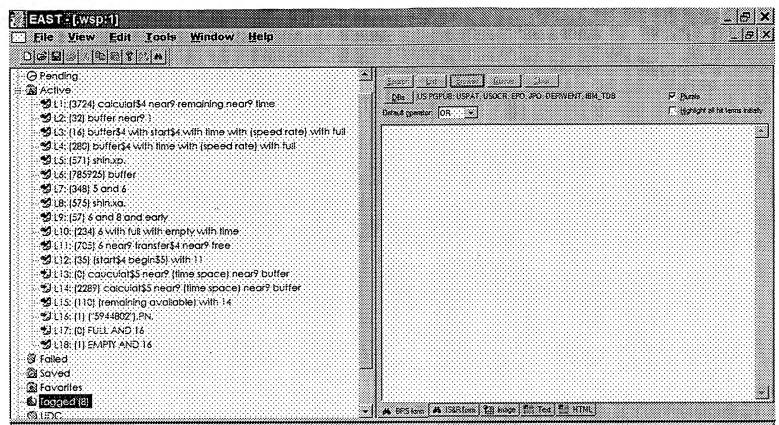


	U	Document ID	Issue Date	Pages	Tifle	Current OR	Current XRef	Retrieval Classi	
	Г	:Ø: US 20040064607.A1	20040401	14	Electronic apparatus and startup contr	710/57			Odakura, Yo
2	Г	Ø US 5944802 A	19990831	11	Dynamic speed matching of hast chan	710/52	710/53:		Bello: Keith /
3	٣	₽ US 6516359 81	20030204	17	Information processing method and a	710/52	709/219;		Kurihara: No
4	Г	₽: US 5450546 A	19950912	43	Intelligent hardware for automatically	710/57	710/1		Krakinan: Cr
5	r	₱: US 5884099:A	19990316	18	Control circuit for a buffer memory to t	710/52	710/57:		XIIngelhofer
6	Г	♥ US 5729708 A	19980317	82	Portable data buffer apparatus with m	710/52			Satoh; Mako
7	Γ	Ø US 5121479 A	19920609	15	Early start mode data transfer apparat	710/34			O'3rlen: Joh
8	Π.	₩ US 5941962 A	19990824	12	Buffer management system for releasin	710/53	710/56;		Hirano; Keiza
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	Г	P	US 20040064607 A1	20040401	14	Electronic apparatus and startup contr. 710/57			Odakura, Ya
2	Г	Þ	US 5944802 A	19990831	11	Dynamic speed matching of host chan 710/52	710/53;		3elio: Keltn /
3	г	ø	US 6516359 B1	20030204	17	Information processing method and a 710/52	709/219;		Kurinara: No
4	г	₽	US 5450546 A	19950912	43	Intelligent hardware for automatically 710/57	710/1		Krakinan: Ch
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